Physics 102 Lab 4: Circuit Algebra and Effective Resistance Dr. Timothy C. Black Spring, 2005

THEORETICAL DISCUSSION

The Junction Rule: Since charge is conserved, charge is neither created or destroyed at any point in the circuit. In particular, at a junction (a point where lines or elements connect together), the total amount of charge entering the junction is the same as the amount leaving the junction. This leads to the so-called *Junction Rule*:

The sum of the currents entering any junction must equal the sum of the currents leaving that junction.

Mathematically, this means that the algebraic sum of the currents at a junction is zero.

$$\sum_{\text{junction}} I_j = I_1 + I_2 + \dots + I_N = 0$$

The Loop Rule: The Loop rule is a consequence of energy conservation. For conservative forces, the change in potential energy U in traversing any closed path is zero. Since the electric potential V is the electric potential energy per unit charge, and the electric force is conservative, it follows that the change in potential in traversing any closed circuit loop is also zero. This leads to the so-called *Loop Rule*.

The algebraic sum of the changes in potential encountered in a complete traversal of any loop of a circuit is zero.

A mathematical statement of this rule is

$$\sum_{\text{loop}} V_j = V_1 + V_2 + \dots + V_N = 0$$

Circuit Elements in Parallel and in Series: Consider figures 1A and 1B. Circuit elements configured as in 1A are said to be connected *in series*. This means that all electrons in the current flow must pass through both element 1 and element 2 as they go from point A to point B in the circuit. Circuit elements configured as in 1B are said to be connected *in parallel*. This means that a given electron in the flow of current may traverse either element 1 or element 2 in going from point A to point B.

Effective Resistance of a Circuit: Any combination of resistors in a circuit, whether in series or in parallel, can always be considered as equivalent to some *effective resistance* in the following sense: If V_{tot} is the total potential drop across the circuit, and I_{tot} is the total current through the circuit, then the effective resistance is defined by

$$R_{eff} = \frac{V_{tot}}{I_{tot}}$$

• Effective Resistance of Parallel Circuits: Consider the circuit diagram of figure 2A. Kirkhoff's loop rule implies that the total change in voltage in traversing the loop from A to B and back to A is zero. Furthermore, the voltage drop from A to B is independent of the path, so that

$$V_{\rm tot} = V_{\rm AB} = V_{\rm path1} = V_{\rm path2}$$



Figure 1-B: Parallel Connection



FIG. 1: Circuit diagram for investigating the relation between potential and current for various circuit elements

Since resistors are ohmic devices and obey Ohm's Law for all V and I, it follows that

$$I_1 = \frac{V_{AB}}{R_1}$$
$$I_2 = \frac{V_{AB}}{R_2}$$

where I_1 and I_2 are the currents through paths 1 and 2, respectively. The effective resistance R_{eff} for the parallel configuration of resistors 1 and 2 is given by

$$R_{\rm eff} = \frac{V_{\rm AB}}{I_{\rm tot}} \tag{1}$$

where the junction rule implies that the total current $I_{\rm tot}$ is given by

$$I_{\rm tot} = I_1 + I_2 = \frac{V_{\rm AB}}{R_1} + \frac{V_{\rm AB}}{R_1} = V_{\rm AB} \left(\frac{1}{R_1} + \frac{1}{R_2}\right)$$

Combining this result with equation 1 gives

$$R_{\rm eff} = \frac{1}{\left(\frac{1}{R_1} + \frac{1}{R_2}\right)}$$

This result is easily generalized. For N resistors in parallel,



Figure 2B: Series resistor configuration

FIG. 2: Parallel and series configurations of a pair of resistors

$$\frac{1}{R_{\text{eff}}} = \frac{1}{R_1} + \frac{1}{R_2} + \dots + \frac{1}{R_N}$$

• Effective Resistance of Series Circuits: Consider now the circuit diagram of Figure 2B. According to the loop rule, the total potential drop from point A to point C is equal to the sum of the potential drop from A to B and the potential drop from B to C, so that

$$V_{\rm tot} = V_{\rm AC} = V_{\rm AB} + V_{\rm BC}$$

Since the same current flows from A to B, from B to C, and from A to C, the total current is equal to the currents through either of the resistors:

$$I_{\rm tot} = I_1 = I_2$$

Then according to the definition of effective resistance,

$$R_{\rm eff} = \frac{V_{\rm tot}}{I_{\rm tot}} = \frac{V_{\rm AB}}{I_1} + \frac{V_{\rm BC}}{I_2}$$

and since resistors are Ohmic devices, it follows that

$$R_{\rm eff} = R_1 + R_2$$

This result can also be generalized to any number of resistors. For N resistors in series,

$$R_{\rm eff} = R_1 + R_2 + \dots + R_N$$

EXPERIMENTAL PROCEDURE

- 1. Use an ohmeter to measure the resistance of resistors R_1 and R_2
- 2. Connect the parallel resistance circuit shown in Figure 3A.
 - Calculate the effective resistance R_{\parallel}^{calc} of this circuit using the measured values of R_1 and R_2
 - Adjust the power supply to give a voltage drop of $V_{AB} \approx 10$ V across points A and B. Use the voltmeter to measure V_{AB} .
 - Use the ammeter to measure the total current I_{tot} through the circuit.
 - Use your measurements of V_{AB} and I_{tot} and Ohm's Law to calculate the experimental value of the effective resistance, R_{\parallel}^{exp} .
 - Numerically compare R_{\parallel}^{calc} with R_{\parallel}^{exp} by calculating their fractional discrepancy.
- 3. Connect the series resistance circuit shown in Figure 3B.
 - Calculate the effective resistance R_{series}^{calc} of this circuit using the measured values of R_1 and R_2
 - Adjust the power supply to give a voltage drop of $V_{AB} \approx 10$ V across points A and B. Use the voltmeter to measure V_{AB} .
 - Use the ammeter to measure the total current I_{tot} through the circuit.
 - Use your measurements of V_{AB} and I_{tot} and Ohm's Law to calculate the experimental value of the effective resistance, R_{series}^{exp} .
 - Numerically compare R_{series}^{calc} with R_{series}^{exp} by calculating their fractional discrepancy.



Figure 3A: Parallel resistor configuration



Figure 3B: Series resistor configuration

FIG. 3: Parallel and series configurations of a pair of resistors